500.42884X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): T. ENDO, et al

Serial No.: 10/608,209

Filed: June 30, 2003

Title: INFORMATION PROCESSING MEANS

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.97 & 1.98

MS DD
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

January 30, 2004

Sir:

In the matter of the above-identified application, applicants are submitting herewith a copy of a communication from a foreign patent office in a counterpart foreign application and copies of the documents listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This information disclosure statement is being submitted before the mailing date of a first office action on the merits..

Each of the documents listed on the attached form equivalent to Form PTO-1449 is in the English language.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus Deposit Account No. 01-2135 (500.42884X00) please credit any excess fees to such deposit account.

Respectfully submitted,

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SHEET 1 OF 1

FORM PTO-1449 U.S. Department of Commerce (Rev. 4/92) Patent and Trademark Office

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use several sheets if necessary)

ATTY. DOCKET NO. 500.42884X00	SERIAL NO. 10/608,209
APPLICANT T. ENDO, et al	
FILING DATE June 30, 2003	GROUP 2121

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER							DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT	
													YES	NO
	0	3	0	0	1	3	6	2	1/2003	PCT				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		B. Boer "A DPA Attack against the Modular Reduction within a CRT Implementation of RSA", Cryptographic Hardware and Embedded Systems, Ches 2002, 4th International Workshop, Revised Papers (Springer Verlag, Lecture Notes in Computer Science, vol. 2523), July 15, 2002, pp. 228-243.
		J. Dhem et al, "A Practical Implementation of the Timing Attack", Smart Card Research and Applications, Third International Conference, Cardis '98 (Springer Verlag, Lectures Notes in Computer Science vol. 1820, Sep. 16, 1998, pages 1-18.

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.